In re Patent Application of:

CROCE ET AL.

Serial No. 09/839,596 Filing Date: 4/20/01

Knass

first conductivity type.

## REMARKS

The Examiner is thanked for helpfully noting that Claim 14 was mistakenly labeled as Claim 15 in the "Version With Markings To Show Changes Made" accompanying Applicants' Amendment filed on November 21, 2002. Accordingly, a new "Version With Markings To Show Changes Made" is attached hereto which includes a corrected marked-up version of Claim 14.

In view of the foregoing and the Amendment filed on November 21, 2002, it is submitted that all of the claims are patentable. Accordingly, a Notice of Allowance is respectfully requested in due course. Should any minor informalities need to be addressed, the Examiner is encouraged to contact the undersigned attorney at the telephone number listed below.

Respectfully submitted,

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## VERSION WITH MARKINGS TO SHOW CHANGES MADE

14. (Amended) A lateral diffused metal oxide semiconductor (LDMOS) integrated device comprising:

a semiconductor substrate;

a drain region of a first conductivity type adjacent said semiconductor substrate and comprising a superficial buffer region being more heavily doped than adjacent portions of said drain region;

said superficial buffer region having a dopant concentration of about  $5 \times 10^{16}$  to  $5 \times 10^{17}$  atoms cm<sup>-3</sup> and the adjacent portions of said drain region having a dopant concentration of about  $2.5 \times 10^{15}$  to  $2.5 \times 10^{16}$  atoms cm<sup>-3</sup>;

a body region [in] <u>surrounded by</u> said superficial buffer region and having a second conductivity type; and

a source region in said body region and having the first conductivity type.

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## CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: DIRECTOR, U.S. PATENT AND TRADEMARK OFFICE, WASHINGTON, D.C. 20231, on this  $27^{+/2}$  day of December, 2002.

Muin & Getterson